

Integfrence Search
EAST Search History

10/808344

Ref #	Hits	Search Query	DBs.	Default Operator	Plurals	Time Stamp
L1	0	(drive adj circuit).clm. and (shift adj register).clm. and (buffer adj circuit).clm. and (n adj channel).clm. and (thin adj film adj transistor).clm. and (depletion adj mode).clm. and (active adj layer).clm.	US-PGPUB	OR	ON	2006/11/25 17:15
L2	0	(drive adj circuit).clm. and (shift adj register).clm. and (buffer adj circuit).clm. and (n adj channel).clm. and (thin adj2 film adj2 transistor).clm. and (depletion adj mode).clm. and (active adj layer).clm.	US-PGPUB	OR	ON	2006/11/25 17:15

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1681	257/66.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 17:09
L2	836	257/291.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 15:52
L3	3233	257/347.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 15:52
L4	308	257/348.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 17:10
L5	403	257/402.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 16:17
L6	786	257/E29.273.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/25 16:17